

Replacement Sheet For

Title: DIGITAL BROADCASTING TRANSMISSION/RECEPTION SYSTEM CAPABLE OF IMPROVING RECEIVING AND EQUALIZING PERFORMANCE AND SIGNAL PROCESSING METHOD THEREOF

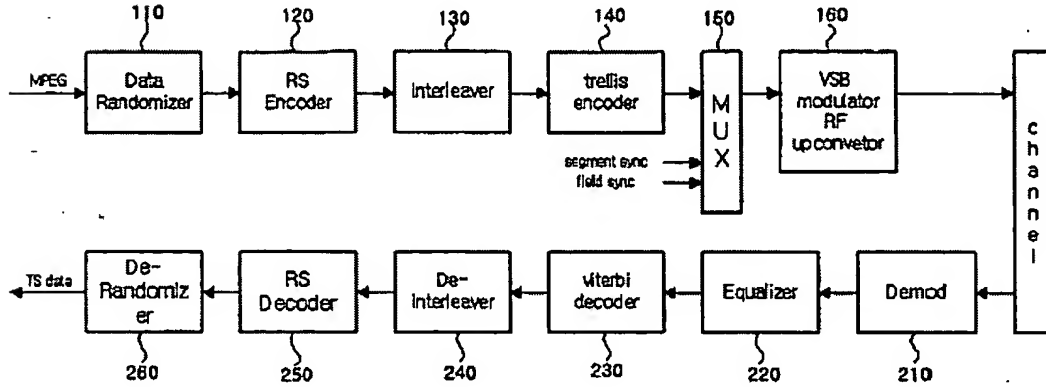
Inventors: Eui-jun PARK et al

Ref: PCT/KR2005/001940 filed June 23, 2005

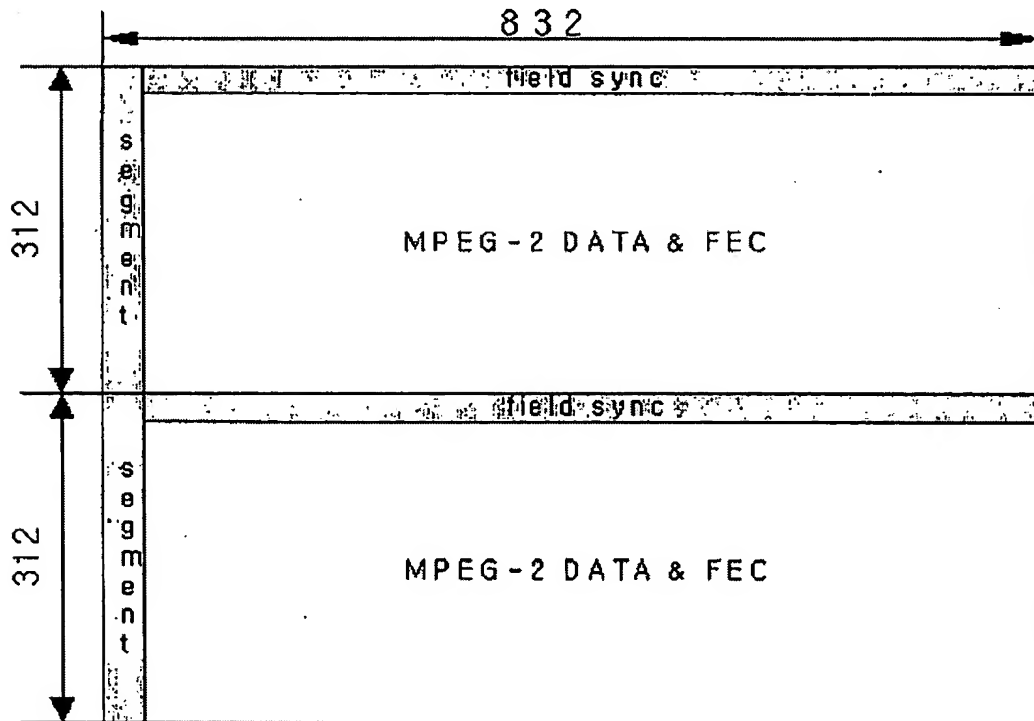
U.S. Filing date: May 3, 2006

Docket No.: 0005.1136

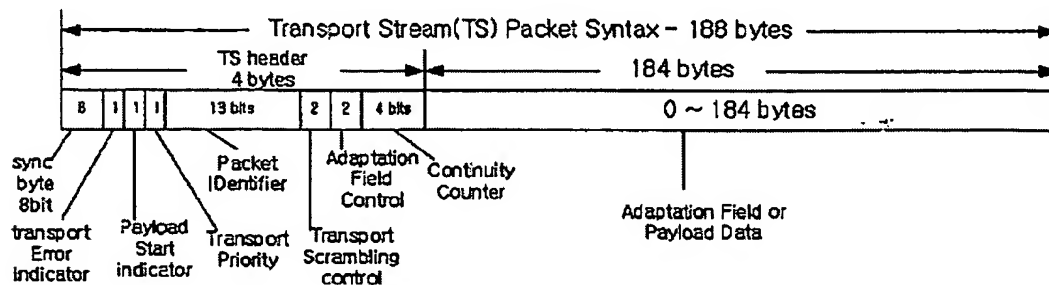
[Fig. 1]



[Fig. 2]



[Fig. 3]



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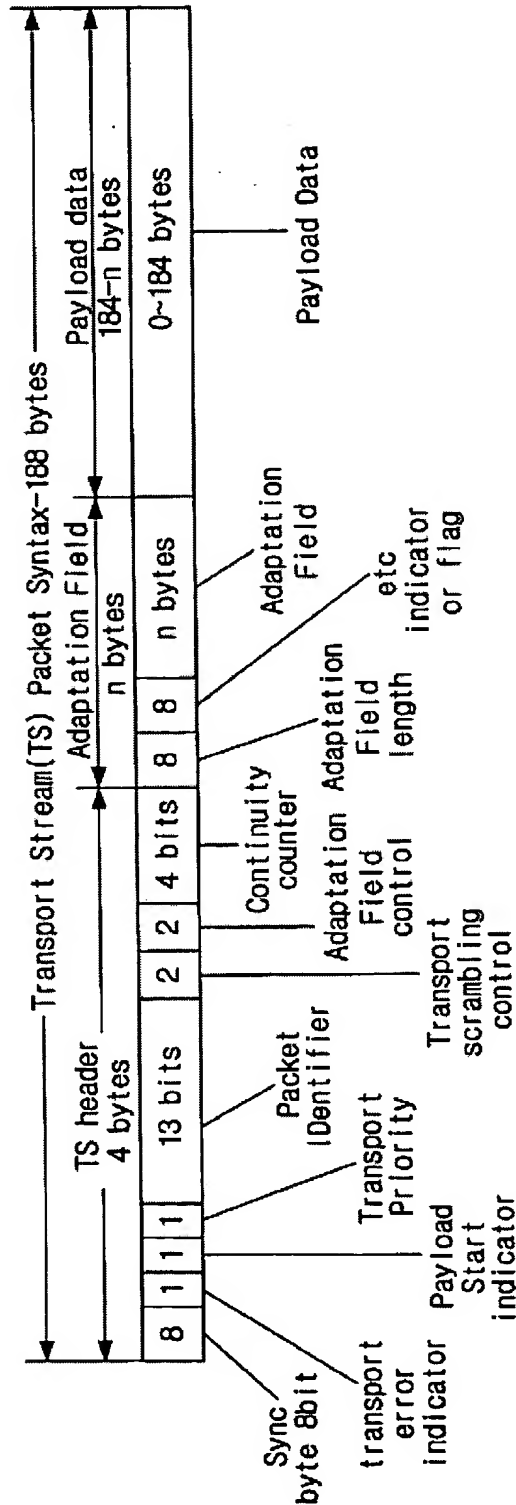
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[Fig.4]



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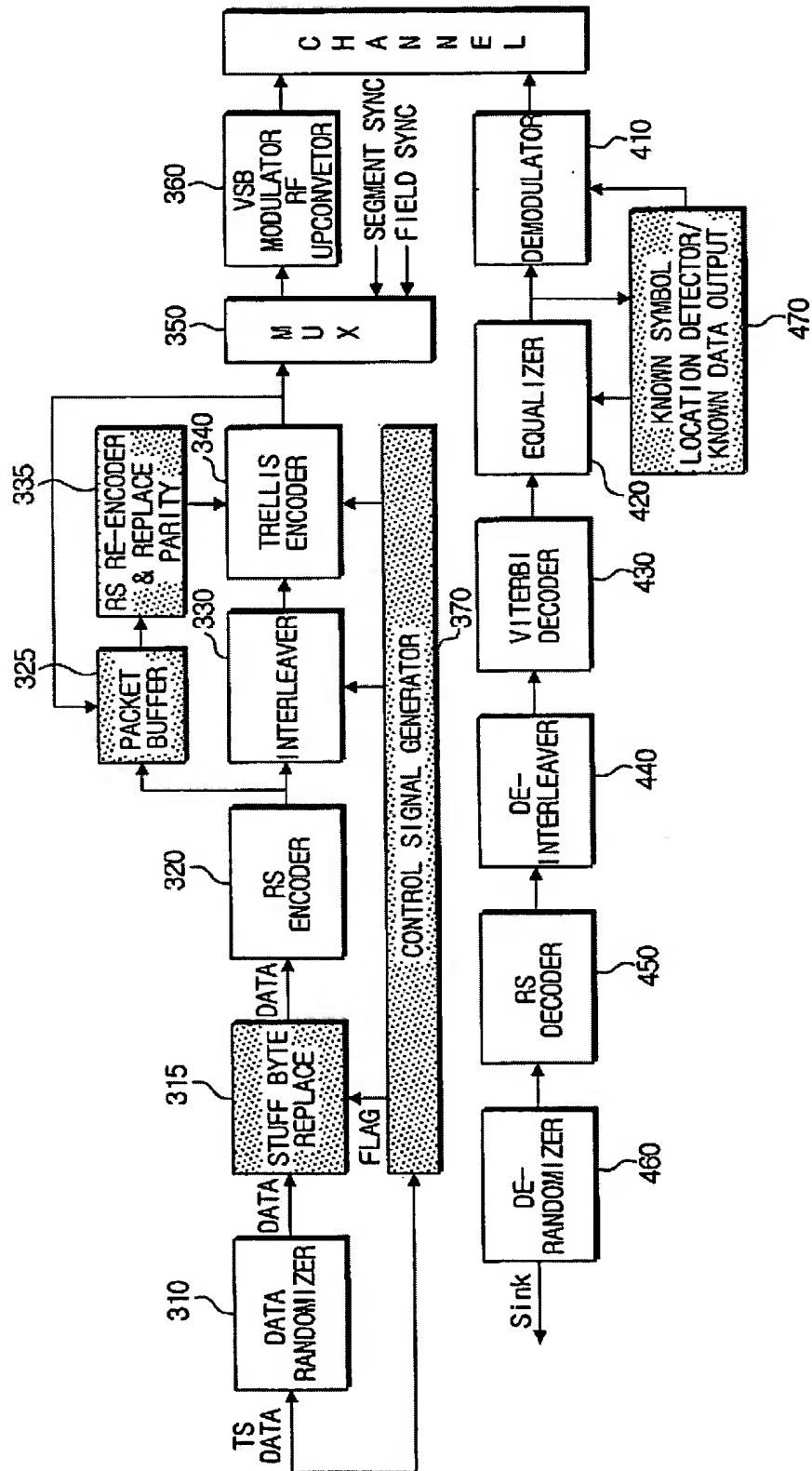
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[Fig. 5]



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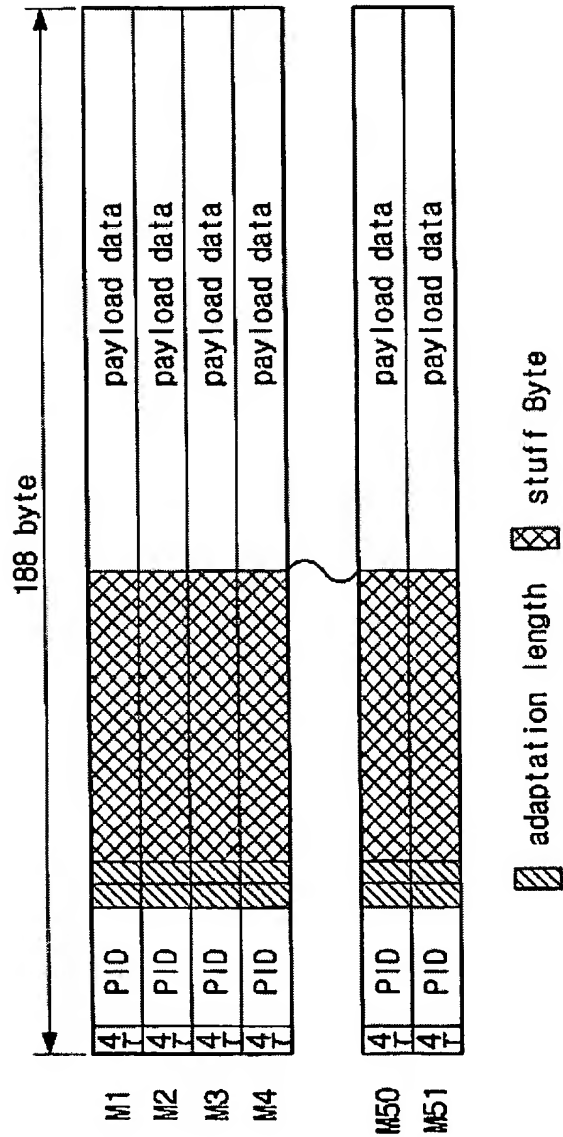
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[Fig. 6]



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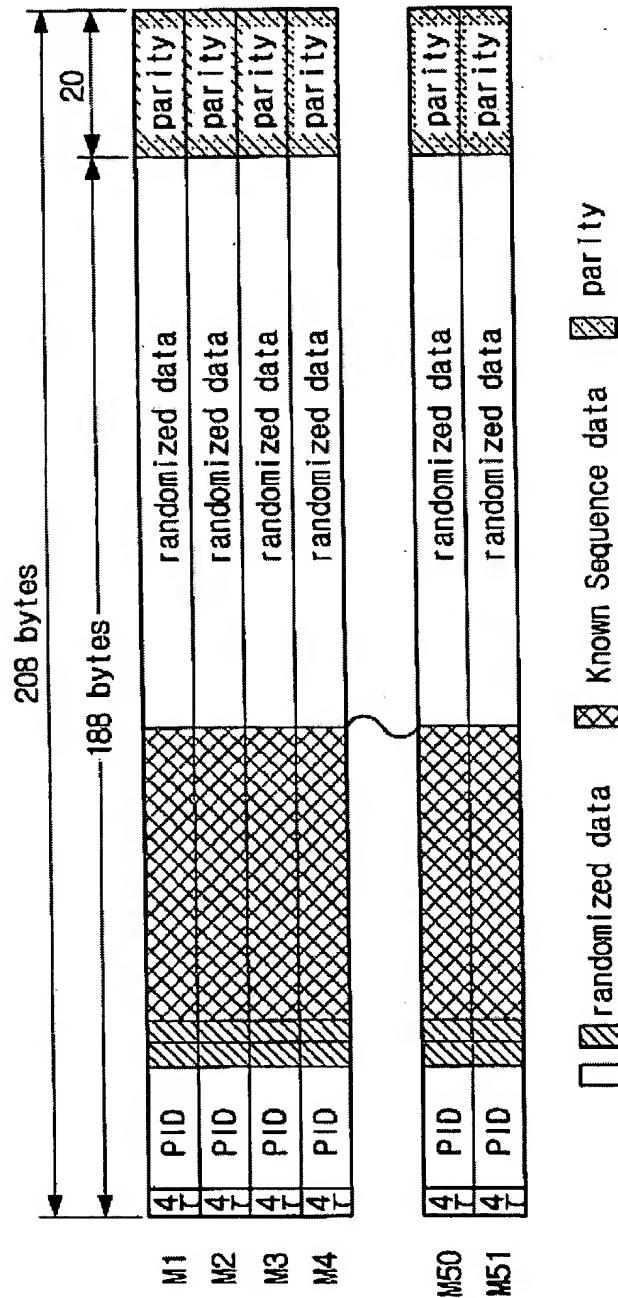
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[Fig.8]



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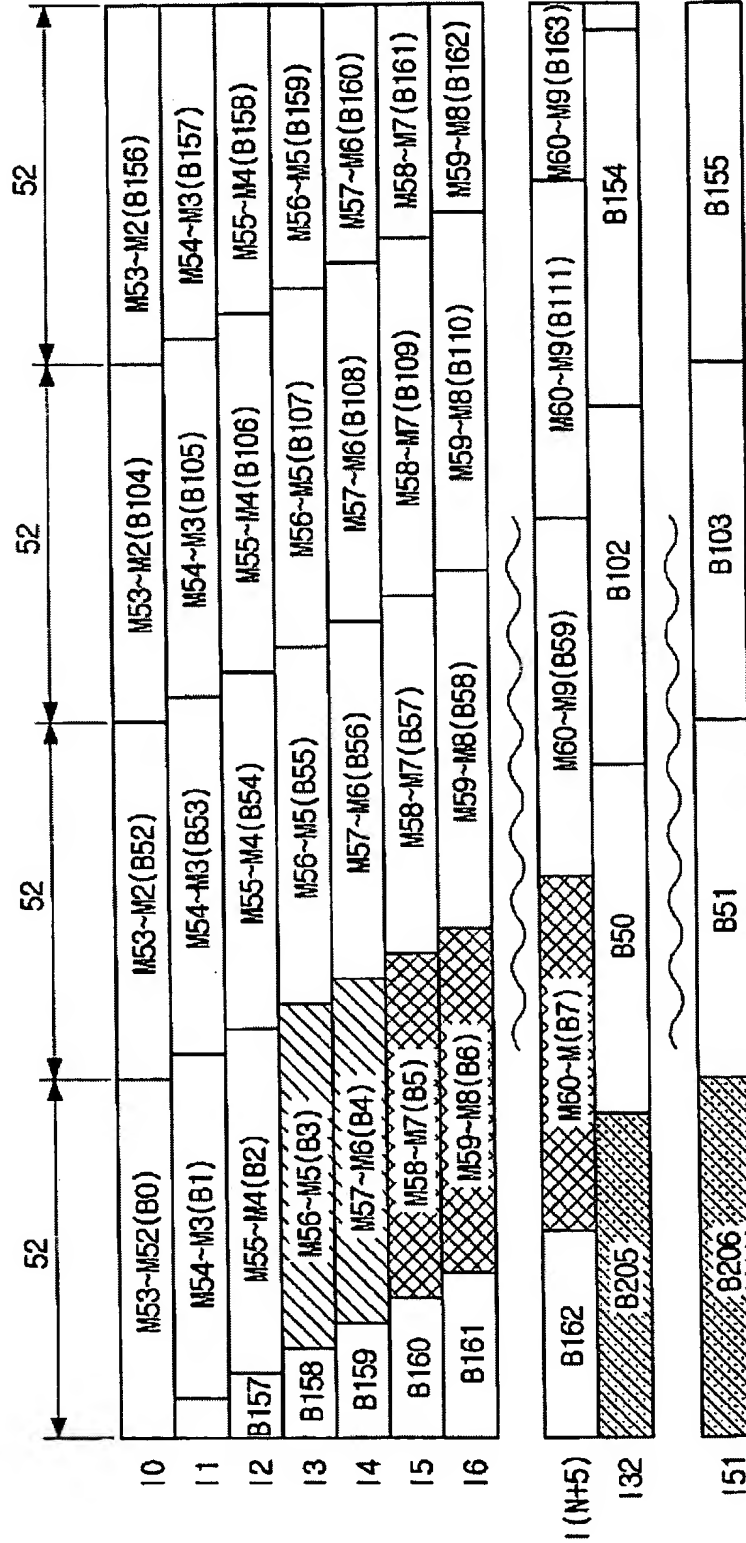
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[Fig. 9]



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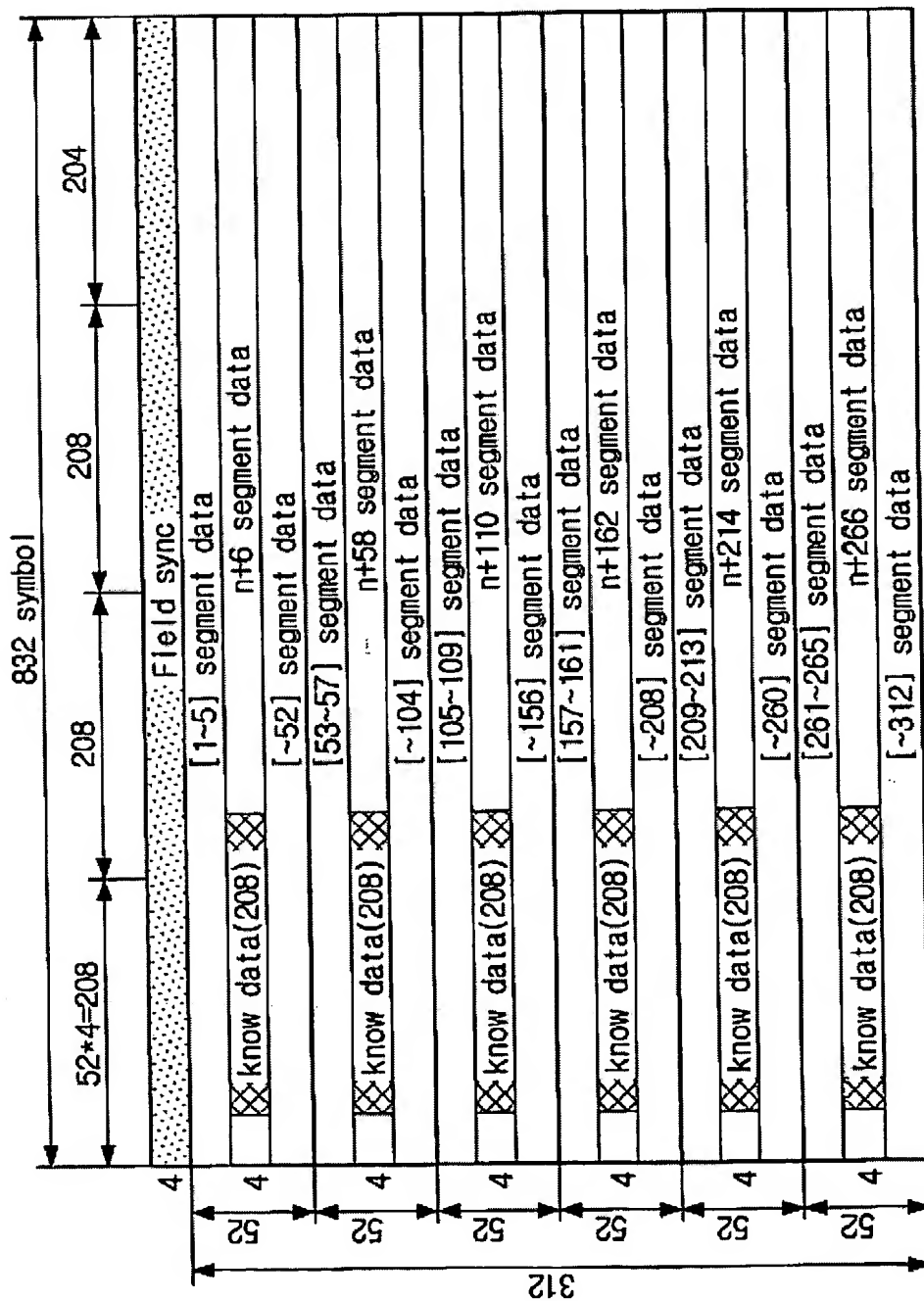
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[Fig. 10]



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[illegible]

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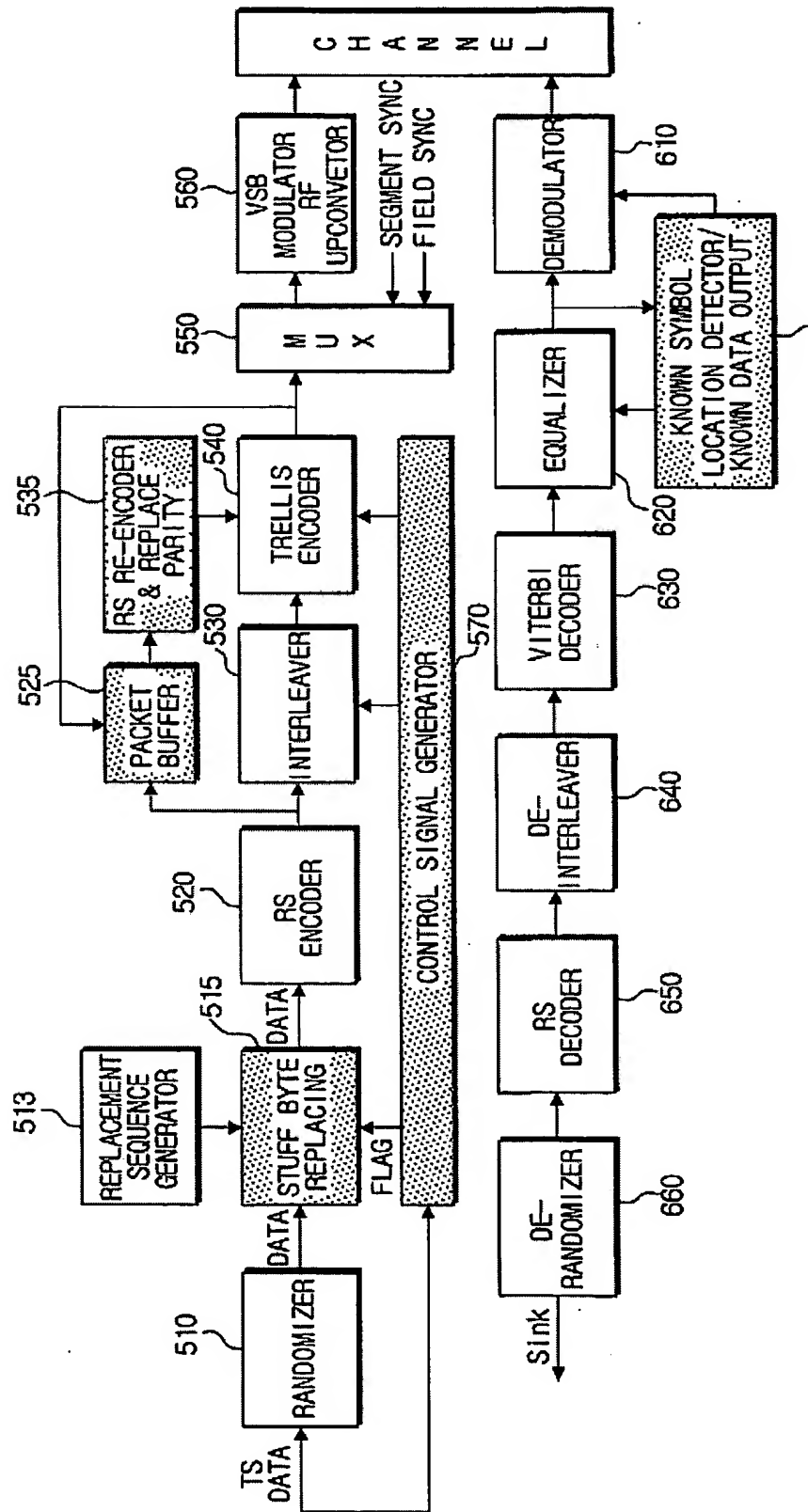
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[Fig. 12]



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Known symbol location detector/known symbol output

The diagram illustrates a known symbol location detector and its output. The detector consists of four main blocks: a 'Known symbol detector' (471), a 'Segments Flag Generation' block (473), a 'Trellis Interleaver' (475), and a 'Known data Sequence' block (477). The process flow is as follows: 1. 'Field segment data' is input to the 'Known symbol detector' (471). 2. The output of the detector is a signal labeled '1' with a duration of 'n'. 3. This signal is input to the 'Segments Flag Generation' block (473), which outputs a signal labeled '2' with a duration of '10'. 4. The signal '2' is input to the 'Trellis Interleaver' (475), which outputs a signal labeled '3' with a duration of '832-204-n symbol'. 5. The signal '3' is input to the 'Known data Sequence' block (477), which outputs a signal labeled '4' with a duration of '204'. Below the blocks, four waveforms are shown, labeled 1, 2, 3, and 4, corresponding to the signals at each stage. Waveform 1 shows a single pulse of duration 'n'. Waveform 2 shows a series of pulses, each of duration '10'. Waveform 3 shows a series of pulses, each of duration '832-204-n symbol'. Waveform 4 shows a series of pulses, each of duration '204'.